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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chian-Min R. Ho, Robert Kristianto Mardjuki, David Lansing Dill, Jing Chyuarn Lin, Ping Fai Yeung, Paul Estrada, II, Jean-Charles Giomi, Tai An Ly, Kalyana C. Mulam, Lawrence Curtis Widdoes, Jr., Paul Andrew Wilcox

Assignee: O-IN Design Automation

Title: Method For Automatically Searching For Functional Defects In A Description Of A Circuit

Serial No.: 09/849,005                      Filing Date: May 4, 2001

Examiner: Not Yet Assigned                      Group Art Unit: Not Yet Assigned

Docket No.: M-5333-1C US

San Jose, California  
July 19, 2001

Attn: Official Draftsperson  
COMMISSIONER FOR PATENTS  
Washington, D. C. 20231

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

Applicants submit 15 sheets of formal drawings, consisting of Figures 1A, 1B, 1C, 1D, 2A, 2B, 2C, 2D, 2E, 2F, 2G, 2H, 3A, 3B, 3C, 3D, 3E, 3F, 3G, 3H, and 4, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (408) 453-9200.

EXPRESS MAIL LABEL NO:

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Respectfully submitted,

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